

Technical Data **Schroff**[®]

Schroff CPCI - Backplanes

part# 23006 – 3nn, 4nn, 7nn & 8nn

Mechanical and Climatic Parameters	Standard	on request
---	-----------------	-------------------

Operating Temperature	-40°C - +105°C	-55°C - +1 55°C
Storage Temperature	-55°C - +105°C	-55°C - +155 °C
Humidity conformal coating	max 95%, not condensing	on request
Flammability: <ul style="list-style-type: none"> • PCB, Connectors • Ceramic caps 	UL 94 V-0 fire-proof	
Connectors <ul style="list-style-type: none"> • Performance level per IEC 61076-4-101 • Mechanical Durability (Mating Cycles) • Total Insertion and Extraction Force (mating) 	IEC 61076-4-101 (HardMetric 2mm Grid) level 2 > 250 cycles < 0,7 N / Pin	level 1 > 500 cycles
Vibration acc. DIN 41640 Part 15	10Hz – 500Hz 5g rms	5Hz – 2000Hz 20g rms
Shock (10 pulses each direction x,y,z)	10g, 6ms	
Low Pressure / Altitude (max Board voltage per single isolation gap doesn't exceed 12V)	no restrictions	
Construction:	10 - Layer Stripline	
Dimensions (mm) <ul style="list-style-type: none"> • Width (pl. see Dwg.) • Height 3U / 6U • Thickness 	20,32mm x # Slots-1mm 128,7mm / 262,05 mm 3,2 mm +/- 0,2 mm	

Electrical Parameters:

Specifications	PICMG 2.0 R3.0 CPCI Core Specification PICMG 2.1 CPCI Hot Swap Specification PICMG 2.6 Bridging Specification PICMG 2.9 System Management Bus Spec. PICMG 2.10 Keying Specification
Service Life: MTBF, acc. to MIL HDBK 217F, cond.: 25°C, ground, benign 6U 8-Slot	more than 600.000h
Characteristic Impedance PCI traces	65 Ω ± 10 %
Ohmic Resistance of Signal Tracks PCI traces	< 95mΩ/Slot
Hot Swap	supported

Technical Data Schroff®

Schroff CPCI - Backplanes

part# 23006 – 3nn, 4nn, 7nn & 8nn

Electrical Parameters (II):

Termination (only 8 Slot Backplanes)	Schottky diodes (on request), pluggable termination board
Power input	<ul style="list-style-type: none"> • Power bugs for wiring or • special Adapter Board to use an ATX cable; this board can act as a power distribution star point within the Systems
max. Current carrying Capacity	8 A per Slot 10 A per Slot
<ul style="list-style-type: none"> • 5V/GND • 3,3V/GND 	
max. Voltage Drop between any two points on the backplane on +5V or +3,3V	< 40mV
VI/O	+5V (default), blue key; 3,3V optional (yellow key)
<ul style="list-style-type: none"> • bridging (default) • on request 	<ul style="list-style-type: none"> • field changeable, using M4 screws and a bus bar • fixed during bp assy by using a Power Bug cable using Faston crimp contacts
Clock frequency	33 MHz 66 MHz up to 5 Slots; on higher Slot number M66EN can be enabled for test purposes (cut a copper link on rear)
PCI Bus Width	32bit; 64bit, check part#
Data Transfer Rate (peak)	132 Mbyte/s (32 bit) / 264 Mbyte/s (64 bit) 264 Mbyte/s (32 bit) / 528 Mbyte/s (64 bit)
Bridging of Backplanes clock frequency: primary / secondary	backplane of slot numbers equal or higher than 4 up to 7 Slots can be bridged
<ul style="list-style-type: none"> • 33 MHz / 33MHz • 66 MHz / 33MHz • 66 MHz / 66MHz 	primary / secondary <ul style="list-style-type: none"> • any slot number as primary and secondary b/p • 4 Slots / any Slot number • 4 Slot / 4 Slot