



Intel® Edison Board Support Package

User Guide

May 2015

Revision 006



Notice: This document contains information on products in the design phase of development. The information here is subject to change without notice. Do not finalize a design with this information.

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

A "Mission Critical Application" is any application in which failure of the Intel Product could result, directly or indirectly, in personal injury or death. SHOULD YOU PURCHASE OR USE INTEL'S PRODUCTS FOR ANY SUCH MISSION CRITICAL APPLICATION, YOU SHALL INDEMNIFY AND HOLD INTEL AND ITS SUBSIDIARIES, SUBCONTRACTORS AND AFFILIATES, AND THE DIRECTORS, OFFICERS, AND EMPLOYEES OF EACH, HARMLESS AGAINST ALL CLAIMS COSTS, DAMAGES, AND EXPENSES AND REASONABLE ATTORNEYS' FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PRODUCT LIABILITY, PERSONAL INJURY, OR DEATH ARISING IN ANY WAY OUT OF SUCH MISSION CRITICAL APPLICATION, WHETHER OR NOT INTEL OR ITS SUBCONTRACTOR WAS NEGLIGENT IN THE DESIGN, MANUFACTURE, OR WARNING OF THE INTEL PRODUCT OR ANY OF ITS PARTS.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined." Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

Intel software products are copyrighted by and shall remain the property of Intel Corporation. Use, duplication, or disclosure is subject to restrictions stated in Intel's Software License Agreement, or in the case of software delivered to the government, in accordance with the software license agreement as defined in FAR 52.227-7013.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

The code names presented in this document are only for use by Intel to identify products, technologies, or services in development that have not been made commercially available to the public, i.e., announced, launched, or shipped. They are not "commercial" names for products or services and are not intended to function as trademarks.

Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order.

Copies of documents which have an order number and are referenced in this document, or other Intel literature may be obtained by calling 1-800-548-4725 or by visiting Intel's website at <http://www.intel.com/design/literature.htm>.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. See http://www.intel.com/products/processor_number for details.

Intel and the Intel logo are trademarks of Intel Corporation in the United States and other countries.

* Other brands and names may be claimed as the property of others.

Copyright © 2015 Intel Corporation. All rights reserved.



Contents

| | | |
|----------|---|-----------|
| 1 | Introduction | 5 |
| 1.1 | The Yocto Project..... | 5 |
| 1.2 | References..... | 6 |
| 1.3 | Terminology | 6 |
| 2 | Build an Intel® Edison Image using bitbake | 7 |
| 2.1 | Build the Intel® Edison native SDK | 9 |
| 3 | Build an Intel® Edison Image with make | 10 |
| 3.1 | Build the Intel® Edison native SDK with the make command..... | 11 |
| 4 | Creating Custom Intel® Edison Images..... | 12 |
| 4.1 | Adding standard Yocto packages in the image..... | 12 |
| 4.2 | Excluding packages from the image | 12 |
| 4.3 | Add third-party packages to the image..... | 12 |
| 4.4 | Write a Yocto recipe from scratch..... | 13 |
| 4.5 | Add a recipe for a systemd service..... | 13 |
| 5 | Customizing the Linux* Kernel | 14 |

Figures

| | | |
|-----------|----------------------------------|----|
| Figure 1. | Building an image | 5 |
| Figure 2 | Linux kernel configuration | 14 |



Revision History

| Revision | Description | Date |
|----------|---|-------------------|
| ww26 | Initial release. | July 7, 2014 |
| ww32 | Improved section about adding external recipes. | August 4, 2014 |
| ww36 | Corrected code example in chapter 4. | September 5, 2014 |
| 001 | First public release. | September 9, 2014 |
| 002 | Corrected file names and file paths in section 3.3. | November 21, 2014 |
| 003 | Minor corrections. | December 1, 2014 |
| 004 | Minor corrections. | December 17, 2014 |
| 005 | Added chapter on using the make command to build images; minor corrections. | February 4, 2015 |
| 006 | Updated commands, based on the latest <i>meta-intel-edison</i> repository. | May 1, 2015 |

§



1 Introduction

This document is for software and system engineers who are building and customizing images, kernels, and native SDKs for the Intel® Edison Development Platform. Precompiled versions of the BSP are available on the Intel website. Users who don't want to modify the default images don't need to read this document.

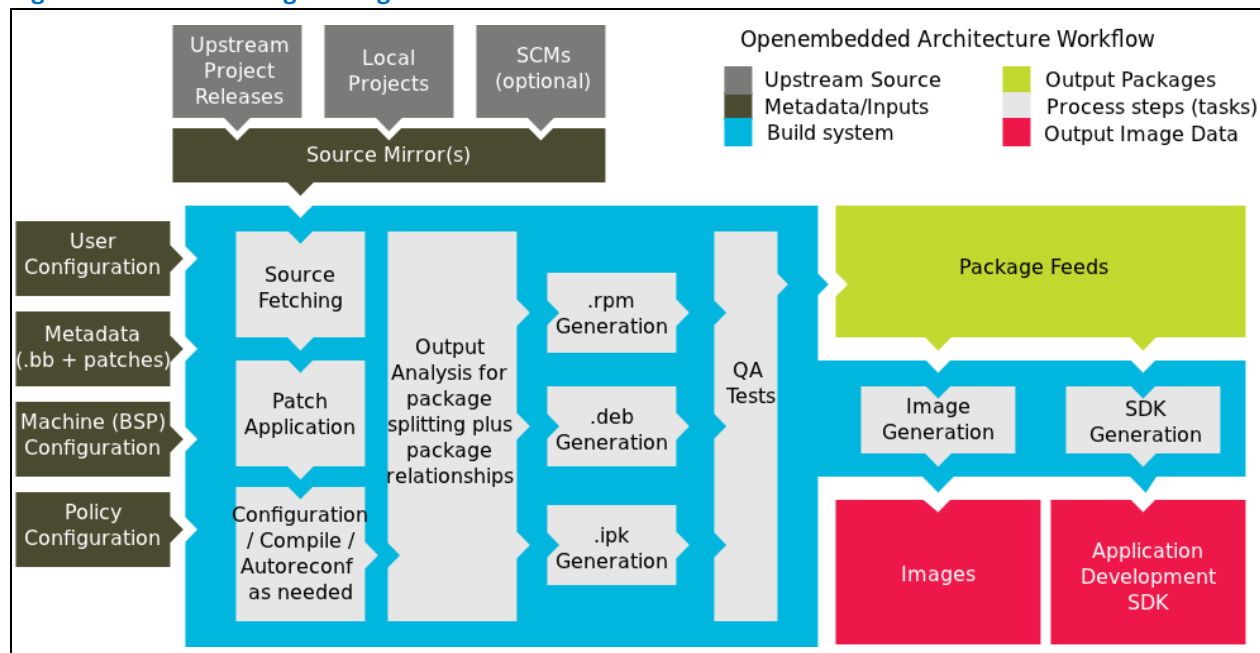
The Intel® Edison Board Support Package offers these features:

- Kernel image based on Linux kernel 3.10.17
- U-boot second stage bootloader
- Bluetooth and Wi-Fi connectivity
- Intel cloud connectivity middleware
- Many base Linux packages provided by the Yocto project

1.1 The Yocto Project

The standard Linux* OS shipped on the Intel® Edison platform is based on Yocto. The Yocto Project is an open source collaboration project that provides templates, tools, and methods to help you create custom Linux-based systems for embedded products.

Figure 1. Building an image



The Intel® Edison BSP source package is the set of Yocto source files necessary to generate a Linux image ready to run on the Intel® Edison board. It contains:

- The set of Yocto recipes describing the process for building a Linux kernel, a bootloader, and a *rootfs*, which together form the bootable images ready to flash on a device.
- The set of Yocto recipes necessary for creating a Software Developer Kit (SDK) and a cross-compiling tool chain that developers can use to create native applications for Intel® Edison.

Note: For details on the Yocto project, consult the documentation on the Yocto website. (See section 1.2.)



1.2 References

| Reference | Name | Number/location |
|-----------|--|--|
| 331188 | Intel® Edison Board Support Package User Guide | (This document) |
| 331189 | Intel® Edison Compute Module Hardware Guide | http://www.intel.com/support/edison/sb/CS-035274.htm |
| 331190 | Intel® Edison Breakout Board Hardware Guide | http://www.intel.com/support/edison/sb/CS-035252.htm |
| 331191 | Intel® Edison Kit for Arduino* Hardware Guide | http://www.intel.com/support/edison/sb/CS-035275.htm |
| 331192 | Intel® Edison Native Application Guide | http://www.intel.com/support/edison/sb/CS-035382.htm |
| 329686 | Intel® Galileo and Intel® Edison Release Notes | https://communities.intel.com/docs/DOC-23388 |
| 332032 | Intel® Edison Software Release Notes | |
| [GSG] | Intel® Edison Getting Started Guide | W: https://communities.intel.com/docs/DOC-23147 M: https://communities.intel.com/docs/DOC-23148 L: https://communities.intel.com/docs/DOC-23149 |
| 331438 | Intel® Edison Wi-Fi Guide | http://www.intel.com/support/edison/sb/CS-035380.htm |
| 331704 | Intel® Edison Bluetooth* Guide | http://www.intel.com/support/edison/sb/CS-035381.htm |
| 332434 | Intel® Edison Audio Setup Guide | |
| [YPQSG] | Yocto Project Quick Start Guide | http://www.yoctoproject.org/docs/current/yocto-project-qs/yocto-project-qs.html |
| [YDM] | Yocto Developer Manual | http://www.yoctoproject.org/docs/current/dev-manual/dev-manual.html |
| [YKDM] | Yocto Kernel Developer Manual | http://www.yoctoproject.org/docs/latest/kernel-dev/kernel-dev.html |

1.3 Terminology

| Term | Definition |
|------|------------------------|
| SSH | Secure shell |
| FTP | File Transfer Protocol |
| GDB | GNU debugger |

§



2 Build an Intel® Edison Image using *bitbake*

Building a standard Intel® Edison image requires downloading and installing several prerequisite packages. These instructions are valid for a recent Ubuntu Linux* distribution and should be valid for other distributions with minor changes.

Note: Make sure your working directory is not part of an encrypted file system, such as **eCryptFS**. Because encrypted file systems restrict file length, the build will fail.

To build a standard Intel® Edison image, do the following:

1. Install the prerequisite packages with the following command:

```
sudo apt-get install build-essential git diffstat gawk chrpath texinfo
libtool gcc-multilib
```

2. Download the BSP source package *edison-src.tgz* from the [Intel® Edison Software Downloads](#) page. The package includes the full Yocto environment, and Intel® Edison-specific Yocto recipes to build the image (including the Linux kernel), a bootloader, and all necessary packages. Download the BSP source package to your working directory and decompress it.

```
tar xvf edison-src.tgz
cd edison-src/
```

3. Use the *setup.sh* script to initialize the build environment for Intel® Edison.

To create download and sstate directory, you can use *mkdir* command:

```
mkdir bitbake_download_dir
mkdir bitbake_sstate_dir
```

```
./meta-intel-edison/setup.sh --dl_dir=<=/<path_to>/bitbake_download_dir
--sstate_dir=<=/<path_to>/bitbake_sstate_dir
```

Optionally, you can move your download and build cache (also called *sstate*) directories from the default location under the build directory, using the *--dl_dir* and *--sstate_dir* options. Doing this will make it easier to share this data between build environments, and allow much faster build and download time when rebuilding the full image, even after a full manual cleanup (by means of deleting everything under your build directory). To create the *sstate* directory, use the *mkdir* command:

```
mkdir bitbake_download_dir
mkdir bitbake_sstate_dir
```

4. Configure the shell environment with the *source* command below. After the command executes, the directory changes to the *edison-src/build* folder.

```
source poky/oe-init-build-env
```

5. Now you are ready to build the full Intel® Edison image with the *bitbake* command:

```
bitbake edison-image
```

Building all the packages from scratch can take up to 5 or 6 hours, depending on your host. After the first build (provided you have not done any major cleanups), you can expect much faster rebuilds, depending on your host and the amount of changes. When the *bitbake* process completes, images to flash are created in the *edison-src/build/tmp/deploy/images/edison* directory. To simplify the flash procedure, run the script below to copy the necessary files to the *build/toFlash* directory.

```
./meta-intel-edison/utils/flash/postBuild.sh
```

The images are ready to flash on the Intel® Edison Development Board. Refer to the [GSG] for details on the flashing procedure.



Build an Intel® Edison Image using bitbake



2.1 Build the Intel® Edison native SDK

To cross-compile native applications for your image, you must generate an SDK containing a cross-compiler toolchain and sysroot. You can generate a full SDK for the Intel® Edison Development Board with the following command:

```
bitbake edison-image -c populate_sdk
```

This *bitbake* command creates the SDK installer script:

```
ls tmp/deploy/sdk  
poky-edison-eglibc-x86_64-edison-image-core2-32-toolchain-1.6.1.sh
```

§



3 Build an Intel® Edison Image with *make*

The Intel® Edison source also supports Makefile-based builds, which allows you to build a standard Intel® Edison image with the *make* command. To build a standard image with the *make* command, do the following:

1. Install the prerequisite packages (if you have not already done so) with the following command:

```
sudo apt-get install build-essential git diffstat gawk chrpath texinfo  
libtool gcc-multilib
```

2. Download the BSP source package *edison-src.tgz* from the [Intel® Edison Software Downloads](#) page. The package includes the full Yocto environment, and Intel® Edison-specific Yocto recipes to build the image (including the Linux kernel), a bootloader, and all necessary packages. Download the BSP source package to your working directory and decompress it.

```
tar xvf edison-src.tgz  
cd edison-src/
```

3. To configure the Yocto build environment, enter the following command:

```
make setup
```

This command automatically creates *download* and *sstate* directories in the *bbcache* directory. You can override the value for the download cache directory or specify the number of threads to use for compilation (to speed up build time) by editing the Yocto configuration file *local.conf*. This file is in *<edison-src>/out/linux64/build/conf/local.conf*.

4. When the setup is ready, you can build the full Intel® Edison image with the *make* command:

```
make image
```

You will find the images in the *<edison-src>/out/current/build/toFlash* directory.

5. Flash the images:

```
make flash
```

To get help on available commands, enter the following:

```
make help
```



3.1 Build the Intel® Edison native SDK with the make command

To cross-compile native applications for your image, you must generate an SDK containing a cross-compiler toolchain and sysroot. You can generate a full SDK for the Intel® Edison Development Board with the following command:

```
make sdk
```

This *make* command creates the SDK installer script in `<edison-src>/out/current/build/tmp/deploy/sdk/`:

```
ls out/current/build/tmp/deploy/sdk/  
poky-edison-eglibc-x86_64-edison-image-core2-32-toolchain-1.6.1.sh
```

Execute the cross-compiler script to install the toolchain.

You can still use the *bitbake* command if you used *make* for basic setup as described above. In order to use the *bitbake*, go to the `<edison-src>/out/current/` directory and configure the shell environment with the *source* command, as shown below.

```
$ cd out/current  
$ source poky/oe-init-build-env
```

After the command executes, the directory changes to the `edison-src/out/linux64/build` folder.





4 Creating Custom Intel® Edison Images

This section explains how to customize standard Linux* images for the Intel® Edison platform.

4.1 Adding standard Yocto packages in the image

Yocto comes with a large set of recipes allowing you to simply add packages to our image. The available packages are on <http://packages.yoctoproject.org>. In order to add a package to our image, you simply need to add it to the `IMAGE_INSTALL` variable. For example, if you want to add the lib PNG to the image, add the following line to the `edison-src/meta-intel-edison/meta-intel-edison-distro/recipes-core/images/edison-image.bb` file:

```
IMAGE_INSTALL += "libpng"
```

Rebuild the image to have `libpng` included in it.

Note: If you need to add patches to existing upstream sources, consult the Yocto documentation [YDM].

4.2 Excluding packages from the image

To exclude unnecessary packages from the image, remove the matching entry from the `IMAGE_INSTALL` variable (see section 4.1) or add the package name to the `PACKAGE_EXCLUDE` variable in the `build/conf/local.conf` file.

```
PACKAGE_EXCLUDE = "package1 package2"
```

4.3 Add third-party packages to the image

If Yocto does not provide a package you need, chances are good that someone else has created a Yocto recipe for it. In this section, we will add a set of Yocto recipes (from `meta-openembedded`, a third-party Yocto layer) to the Intel® Edison source. The recipes contained in this layer allow you to add many packages in a custom Intel® Edison image. Download the `meta-openembedded` layer at: <https://github.com/openembedded/meta-openembedded>. As an example, the `opencv` library will be added to the image. The example assumes a standard image has been created by running the `setup.sh` script and `bitbake edison-image` as described in the previous sections.

1. Get the OpenEmbedded Yocto layer collection from GitHub. We use the "daisy" branch matching the version of Yocto that is used by the Intel® Edison software.

```
cd edison-src/meta-intel-edison
git clone https://github.com/openembedded/meta-openembedded.git
cd meta-openembedded
git checkout daisy
```

2. Tell `bitbake` to look for recipes contained in the new `meta-openembedded` layer. Edit the `edison-src/build/conf/bblayers.conf` file and append the path to the new layer into the `BBLAYERS` variable:

```
BBLAYERS ?= " \
[.]
Full/path/to/edison-src/meta-intel-edison/meta-openembedded/
meta-openembedded \ "
```

3. You now can add any recipe provided by the new `meta-oe` layer to your image. As in section 4.1, to add `opencv` to the image, add it to the `IMAGE_INSTALL` variable. You can do this in the `edison-src/meta-intel-edison/meta-intel-edison-distro/recipes-core/images/edison-image.bb` file, for example. In the particular case of `opencv`, to avoid bringing too many dependencies, you should also redefine a specific variable so that the library is built without `gtk` support:

```
IMAGE_INSTALL += "opencv"
PACKAGECONFIG_pn-opencv="eigen jpeg libav png tiff v4l"
```



4. Save the file and rebuild the image as follows:

```
cd edison-src
source poky/oe-init-build-env
bitbake edison-image
```

4.4 Write a Yocto recipe from scratch

It is also possible to create your own Yocto recipes from scratch and add them to the image. This section describes the required steps to add a *hello_world* C program to our image. The GNU *hello_world* is a real project that you can download from <http://ftp.gnu.org/gnu/hello/hello-2.7.tar.gz>.

1. The first step is to tell *bitbake* where to download the code, and how to build the package. This is done by adding a new recipe (*.bb*) file in the right directory. To do this, create the recipe file *hello_2.7.bb* in the *edison-src/meta-intel-edison/meta-intel-edison-distro/recipes-support/hello* directory, with the following content:

```
DESCRIPTION = "GNU Helloworld application"
LICENSE = "GPLv3+"
LIC_FILES_CHKSUM = "file://COPYING;md5=d32239bcb673463ab874e80d47fae504"
SRC_URI = "${GNU_MIRROR}/hello/hello-${PV}.tar.gz"
SRC_URI[md5sum] = "fc01b05c7f943d3c42124942a2a9bb3a"

inherit autotools gettext
```

Note: As the *hello_world* project makes use of the autotools, it is enough to inherit the autotool Yocto class to tell *bitbake* how to configure and build the project. Refer to the [YDM] for details on the *.bb* syntax.

2. The *hello world* recipe is ready, but you still need to add it to your image. To do so, add the following line to the *edison-src/meta-intel-edison/meta-intel-edison-distro/recipes-core/images/edison-image.bb* file:

```
IMAGE_INSTALL += "hello"
```

3. Then rebuild the image:

```
bitbake edison-image
```

4.5 Add a recipe for a systemd service

Developers may choose to add their own application as a service to Intel® Edison. On the Intel® Edison platform, services are special applications that run in the background. They are managed by *systemd*, a system and service manager for Linux*.

A *systemd* service is described by a *.service* file that needs to be deployed on the Intel® Edison board usually in */lib/systemd/system*. This service file contains information on how and when to start the service, which are its dependencies, etc.

Note: Refer to *systemd* documentation <http://www.freedesktop.org/wiki/Software/systemd> for an overview of the base *systemd* concepts, and a description of the associated tools.

The Intel® Edison BSP source includes a sample recipe for creating a *systemd* service application using Yocto. The sample is in the *edison-src/meta-intel-edison/meta-intel-edison-distro/recipes-support/watchdog-sample* folder.

A system service is described by a *.service* file. Refer to the sample file *watchdog-sample.service* at: <http://www.freedesktop.org/software/systemd/man/systemd.service.html>.

To deploy the service from a Yocto recipe, you need to inherit the Yocto *systemd* class. Refer to <http://www.yoctoproject.org/docs/current/ref-manual/ref-manual.html#ref-classes-systemd>.

§

5 Customizing the Linux* Kernel

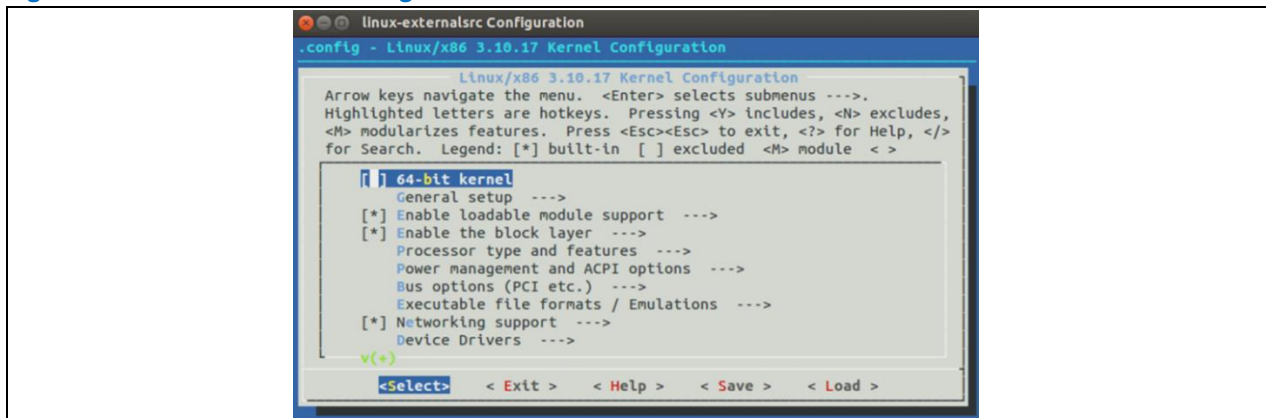
This chapter contains a brief overview of making kernel modifications. Customizing the kernel is important on embedded systems for making new devices and sensors. For more detailed information, see the [YKDM] at: <http://www.yoctoproject.org/docs/latest/kernel-dev/kernel-dev.html>. Check it out for additional ways of configuring the kernel, for example through using more compact and modular configuration fragments. The approach described here is good for ad-hoc modifications while config fragments are shorter than full kernel configuration, and it allows you to create and distribute your own Yocto recipes for modifying specific kernel features.

The base kernel config file is delivered with *edison-src.tar.gz* and is located in the *edison-src/meta-intel-edison/meta-intel-edison-bsp/recipes-kernel/linux/files/defconfig* file.

The *menuconfig* tool provides an easy interactive method with which to define kernel configurations. For general information on *menuconfig*, see <http://en.wikipedia.org/wiki/Menuconfig>. The following command opens the *menuconfig* terminal for configurations:

```
bitbake virtual/kernel -c menuconfig
```

Figure 2 Linux kernel configuration



When the configuration is completed, replace *defconfig* with *.config*, then rename it back to *defconfig*. We also suggest taking a backup of the *defconfig* file. Force bitbake to copy the modified *defconfig* file to the actual build directory. Then the new image with modified kernel is ready to build.

```
cp <path_to_edison-src>/build/tmp/work/edison-poky-linux/linux-yocto/3.10.17+gitAUTOINC+6ad20f049a_c03195ed6e-r0/linux-edison-standard-build/.config <path_to_edison-src>/meta-intel-edison/meta-intel-edison-bsp/recipes-kernel/linux/files/defconfig
```

We can also change the Intel® Edison kernel configuration (*i386_edison_defconfig*) file and overwrite with customized kernel configuration by doing following:

```
cp <path_to_edison-src>/build/tmp/work/edison-poky-linux/linux-yocto/3.10.17+gitAUTOINC+6ad20f049a_c03195ed6e-r0/linux-edison-standard-build/.config <path_to_edison-src>/build/tmp/work/edison-poky-linux/linux-yocto/3.10.17+gitAUTOINC+6ad20f049a_c03195ed6e-r0/linux/arch/x86/configs/i386_edison_defconfig

bitbake virtual/kernel -c configure -f -v
bitbake edison-image
```